

Fig. 1 Related Art

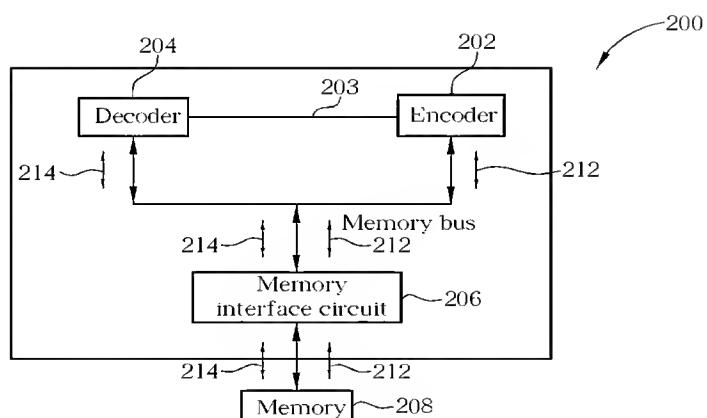


Fig. 2

Picture sequence	0	1	2	3	4	5	6	7	8	9	10	11
Decoder	I0 (0)	P1 (1)	B2 (2)	I3 (0)	P4 (1)	B5 (2)	I6 (0)	P7 (1)	B8 (2)	I9 (0)	P10 (1)	B11 (2)
Encoder	I0 (0)	P1 (1)	I2 (0)	B3 (2)	P4 (1)	I5 (0)	B6 (2)	P7 (1)	I8 (0)	B9 (2)	P10 (1)	I11 (0)
The sum of the average memory bandwidth load	0	2	2	2	2	2	2	2	2	2	2	2

Fig. 3

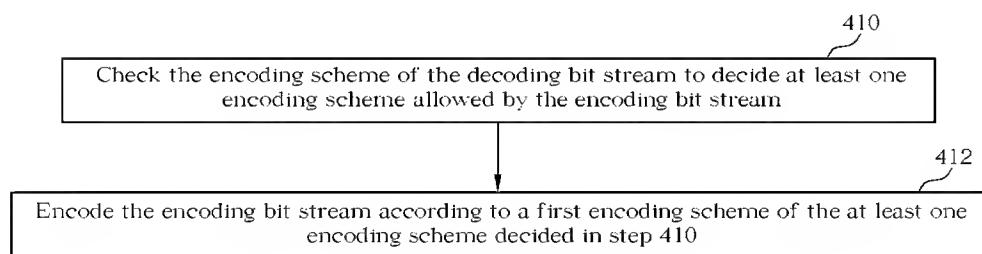


Fig. 4